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APPLICANTS

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** CONTINUING DATA ***** *MS*

** FOREIGN APPLICATIONS ***** *MS*
JAPAN 2001-057305 03/01/2001

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>				

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TITLE
Method of testing a semiconductor integrated circuit and method and apparatus for generating test patterns

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